RPiExtension

Version <1.0>

Revision History

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| --- | --- | --- | --- |
| **Date** | **Version** | **Description** | **Author** |
| 24/Oct/21 | 1.0 | Iteration plan | Altay Brusan |
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# Introduction

This document materializes the road map and milestones of the RPieExt project

## Purpose

The aim of this document is to unify the team members understanding from the steps we planned toward the end of the project.

## Scope

The iteration plan is limited to RPiExt circuit.

## Definitions, Acronyms, and Abbreviations

RPiExtension: Synchronization Box. A unified hardware platform for radiology scanner.

## References

Board datasheet:

https://datasheets.raspberrypi.com/cm4io/cm4io-datasheet.pdf

## Overview

In the following the iteration plan, including timelines, resources and budget are discussed.

# Plan

The hardware architecture is a single board with HDMI, USB, LAN, MicroSD sockets. These sockets are supported by protection and data transceivers.

|  |  |  |
| --- | --- | --- |
| Activity | Start Date | End date |
| Design and implement microcontroller board | 22 Nov 2021 | 29 Nov 2021 |

# Resources

For the first iteration:

* Staffing resources: One electronic engineer
* Financial resource: 2000 USD
* Equipment and Facility: benchtop power source, oscilloscope, bill of materials used in circuit design.

# Evaluation Criteria

The dimensions must fit into the designed housing. The CE and EMC standards must be passed. Also, medical software grades compatibility is required.